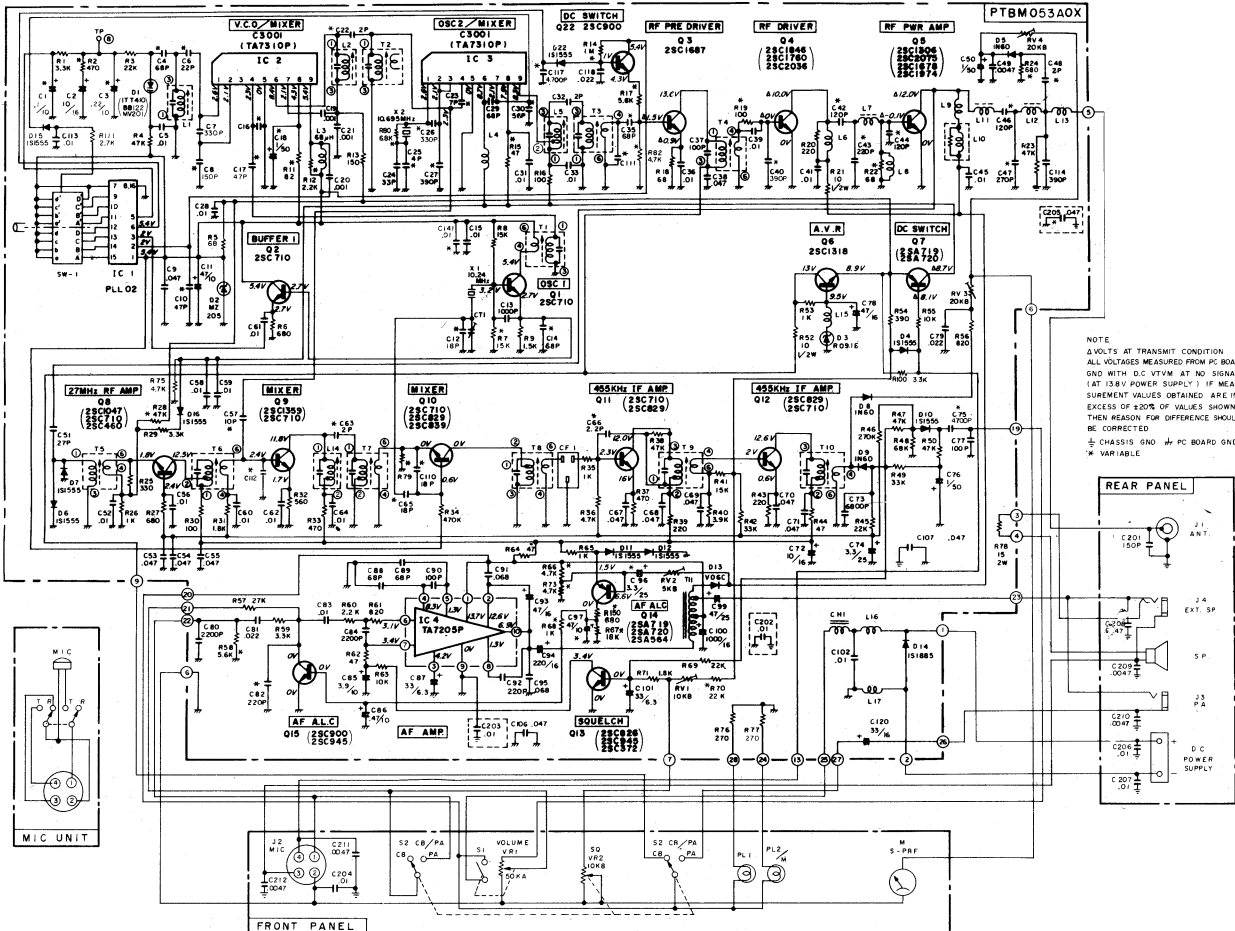
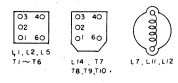


BUMBLE BEE SCHEMATIC DIAGRAM

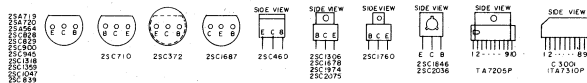


NOTE:
 1. VOLTS AT TRANSMIT CONDITION.
 ALL VOLTAGES MEASURED FROM P.C. BOARD
 GND WITH D.C. VTVM AT NO SIGNAL
 (AT 13.8V POWER SUPPLY). IF MEASUREMENT
 VALUES OBTAINED ARE IN EXCESS OF 20% OF
 VALUES SHOWN, THEN REASON FOR DIFFERENCE
 SHOULD BE CORRECTED.
 2. CHASSIS GND = P.C. BOARD GND.
 3. VARIABLE.

TRANSFORMER TERMINATION INFORMATION (BOTTOM VIEW)



SEMICONDUCTOR TERMINAL CONNECTION (BOTTOM VIEW UNLESS OTHERWISE NOTED)



E: EMITTER
 C: COLLECTOR
 B: BASE

1: 12-18V
 2: 12-18V
 3: 12-18V
 4: 12-18V
 5: 12-18V
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 94: 12-18V
 95: 12-18V
 96: 12-18V
 97: 12-18V
 98: 12-18V
 99: 12-18V
 100: 12-18V

PC BOARD DETAIL

