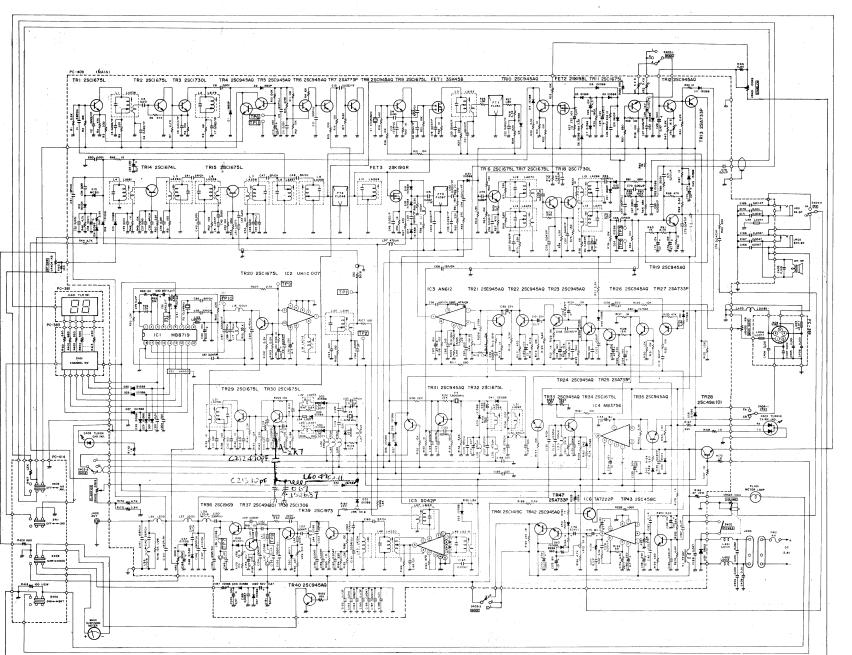
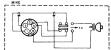
CIRCUIT DIAGRAM FOR GRANT





- RESISTANCE VALUES ARE SHOWN IN O UNLESS OTHERWISE NOTED.
- 2. RESISTOR WATTAGES ARE I/BW UI
- 3. CAPACITANCE VALUES ARE INDICATED | MICROFARADS UNLESS OTERWISE SPEC
- (P= MICRO-MICROFARAD)
- 4. ALL CAPACITORS TEMPERATURE CHARACTERISTICS ARE YO UNLESS OTHERWISE NOTED.

BLOCK DIAGRAM FOR GRANT

