

CIRCUIT DIAGRAM FOR WASHINGTON

OV Noise FRL OV ^{LINEAR} SUPP S OV 7800 FTL

Nfc

VCO Voltage

VCO Voltage

LSB IF

USB AM TX IF

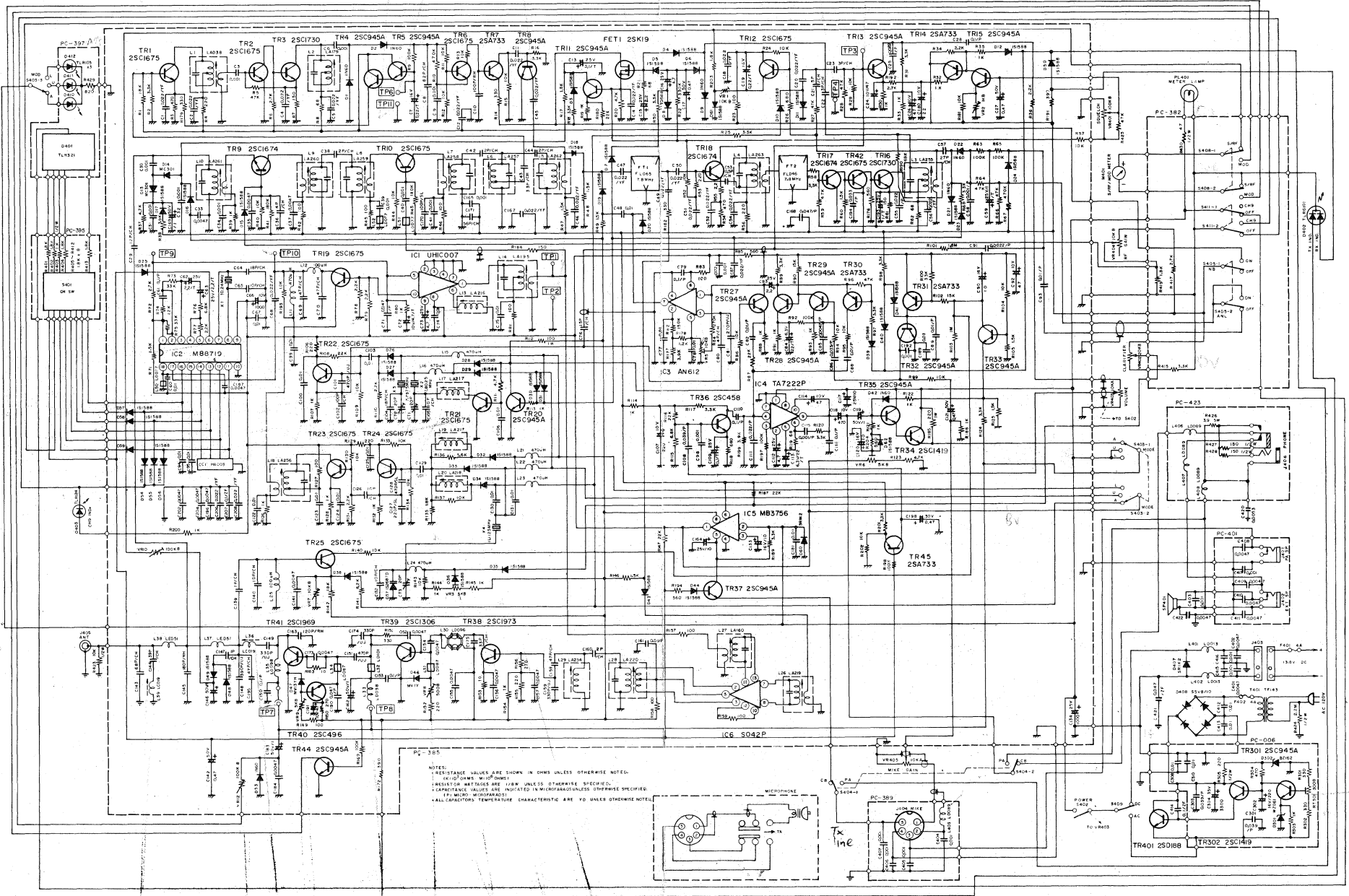
AM TX IF

TX

ALC

FL

Final Current



NOTES:
 * RESISTANCE VALUES ARE SHOWN IN OHMS UNLESS OTHERWISE NOTED.
 * CAPACITANCE VALUES ARE IN MICROFARADS UNLESS OTHERWISE SPECIFIED.
 * ALL CAPACITORS TEMPERATURE CHARACTERISTIC ARE 50 UNLESS OTHERWISE NOTED.

adj
 Mode Meter
 Adj Final Driver Bias Current
 Driver Bias
 AM freq
 TX freq
 LSB freq

AMC

Voltage

BLOCK DIAGRAM FOR WASHINGTON

